The Global Packing Number of a Fat-tree Network

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Abstract—Data centers play an important role in today’s Internet development. Research to find scalable architecture and efficient routing algorithms for data center networks has gained popularity. The fat-tree architecture, which is essentially a folded version of a Clos network, has proven to be readily implementable and is scalable. In this paper, we investigate routing on a fat-tree network by deriving its global packing number and by presenting explicit algorithms for the construction of optimal, load-balanced routing solutions.

Consider an optical network that employs Wavelength Division Multiplexing in which every user node sets up a connection with every other user node. The global packing number is basically the number of wavelengths required by the network to support such a traffic load, under the restriction that each source-to-destination connection is assigned a wavelength that remains constant in the network. In mathematical terms, consider a bidirectional, simple graph, G and let N \subseteq V(G) be a set of nodes. A path system \mathcal{P} of G with respect to N consists of |N|(|N| - 1) directed paths, one path to connect each of the source-destination node pairs in N. The global packing number of a path system \mathcal{P}, denoted by \phi(G, N, \mathcal{P}), is the minimum integer k to guarantee the existence of a mapping \phi : \mathcal{P} \to \{1, 2, \ldots, k\}, such that \phi(P) \neq \phi(P') if P and P' have common arc(s). The global packing number \phi(G, N), denoted by \Phi(G, N), is defined to be the minimum \phi(G, N, \mathcal{P}) among all possible path systems \mathcal{P}. In additional to Wavelength Division optical networks, this number also carries significance for networks employing Time Division Multiple Access (TDMA).

In this paper, we compute by explicit route construction the global packing number of (\mathcal{T}_n, N), where \mathcal{T}_n denotes the topology of the n-ary fat-tree network, and N is considered to be the set of all edge switches or the set of all supported hosts. We show that the constructed routes are load-balanced and require minimal link capacity at all network links.

Index Terms—Global packing number, Fat-tree networks, Clos networks, Latin squares, Load-balancing

I. INTRODUCTION

As data center networks (DCNs) assume an increasingly important role in the Internet, interests on their architecture have built up steadily in the research community. Three models stand out among a host of proposed options: three-tier, fat-free, and DCell [1]. The fat-tree architecture is a folded version of a Clos network, which was originally designed for circuit switching [2]. The fat-tree architecture has subsequently been applied to parallel computing [3]. Its application to DCN has been proposed by S. B. Alexander et al. in [4]. Compared to the legacy three-tier model, the Clos network design enjoys a significant cost benefit and is scalable in regard to the number of connected servers. Google for example has adopted the Clos network framework in its DCN architecture design [5].

In this paper, we investigate the routing problem for an arbitrary fat-tree network under the assumption that every leave node in the tree has a communication connection with identical data rate to any other leave node. Although this fully loaded traffic scenario may not arise frequently, it can be regarded as a worst-case scenario as well as a reference point for more realistic traffic load analysis.

There are two main results reported in this paper. First, we derive the global packing number of a fat-tree network. Second, we explicitly construct routing solutions which achieve the global packing number while possessing good load-balanced property.

The global packing number was originally motivated by an interest to evaluate the minimum number of wavelengths required for an all-optical network [4], [6]. The network employs Wavelength Division Multiplexing (WDM) so that multiple communication connections can share an optical link at the same time by using distinct wavelengths. Although wavelength conversion is feasible, in the majority of all-optical networks there is a wavelength continuity constraint so that the same wavelength will be used for transmission over all the traversed links from source to destination. A route with a fixed wavelength will be referred to as a lightpath.

Given a network traffic pattern, one can construct different sets of lightpaths to satisfy the requirement. The global packing number is the minimum number of wavelengths required to support all the lightpaths. For a ring network, the global packing number has been solved in [7] and later in [8], assuming a uniform load for all source-destination pairs. In this paper, we compute the global packing number of a fat-tree network through explicit construction of routing paths and wavelength assignment. Moreover, we will show that the global packing number for a fat-tree network is identical to the maximum of all its link loads. For more global packing number results, please refer to static Routing and Wavelength
Assignment (RWA) problems [9], [10], [11], [12].

The global packing number has relevance beyond all-optical networks and is connected to network flows for example. A flow in a network refers to a sequence of packets from a given source-destination pair travelling over a fixed route. Packet forwarding of the flow can be implemented through a flow identifier embedded in the header of each individual packet in the flow. An intermediate node reads the flow identifier and looks up a forwarding table to determine how the packet should be forwarded to the next node. The flow identifiers need not be uniquely assigned to all flows. As long as two flows do not share any common link, it is in principle feasible for them to share the same identifier. The global packing number can be viewed as the maximum number of flow identifiers required for a given traffic pattern.

A third interpretation of the global packing number comes from zero-queueing delay routing as investigated under the DCN model in [13]. Consider a DCN in which the links between routers are divided into periodic frames composed of a fixed number of equal duration time slots. Due to the layered structure of a fat-tree network, one can group time slots from different links and assign them to the same flow so that packets from the flow can travel from source to destination without being buffered at any intermediate nodes, in other words, with zero-queueing. The global packing number can be used to define the minimal frame size so that, all flows enjoy the zero-queueing property.

We conclude this section by noting that the blocking problem on a 3-stage Clos network has been extensively studied in the literature, under various definitions of blocking, (such as strictly nonblocking, wide-sense nonblocking and rearrangeably nonblocking,) link condition, (such as single rate or multirate), and traffic models, (such as permutation routing.) We refer the readers to [14], [15], [16], [17] and [18], and the references contained therein for details. Although a small part of the results reported here concerning the global packing number could be derived based on these classical results, we would like to emphasise on two major differences. First, the fat-tree networks we consider in this paper are folded 5-stage or 7-stage Clos networks, so traditional 3-stage network results may or may not carry over. Second, our paper focuses on constructive, load-balanced solutions, which are not explicitly guaranteed by applying classical results. To make the load-balanced concept precise, we introduce the definition of a well-balanced routing solution in the following section.

The structure of the paper is as follows. After the introduction in Section I, we specify the notation used in the paper and provide a preliminary discussion of relevant basic concepts in Section II, along with some quick reference to useful combinatorial tools. Routing solution construction and global packing number computation will be presented in Section III and Section IV, where Section III focuses on the Edge-to-Edge traffic model and Section IV on the Host-to-Host traffic model. Section V is devoted to discussion of the relationship between global packing number and the maximum link load. A conclusion is presented in Section VI.

II. PRELIMINARIES

A. Global packing

Let \( G \) be a connected simple graph with node set \( V(G) \) and a link set \( E(G) \). By putting two opposite arcs on each link of \( G \) we derive a bidirected graph. Let \( A(G) \) be the set of arcs in the bidirected graph. Obviously, \( |A(G)| = 2|E(G)| \). If there is no danger of confusion, we use \( G = (V, A) \) to denote the bidirected graph and \( G = (V, E) \) to denote the underlying simple graph. The global packing number is defined on a bidirected graph.

Let \( G = (V, A) \) be a bidirected graph and \( N \subseteq V(G) \) be a set of nodes. For any two nodes, \( a, b \in N \), one can assign a particular directed path (or dipath) starting from node \( a \) to node \( b \) and denote the set of arcs in the dipath by \( P_{(a, b)} \). Node \( a \) and node \( b \) are called the starting node and ending node of \( P_{(a, b)} \), respectively. Note that \( P_{(a, b)} \) is sometimes set to be one of the shortest paths from \( a \) to \( b \). Let

\[ \mathcal{P} := \{ P_{(a, b)} : a, b \in N, a \neq b \} \]

denote a set of assigned dipaths, one for each distinct ordered node pair. We say \( \mathcal{P} \) is a path system of \( G \) with respect to \( N \), and let \( \mathcal{P}_{G,N} \) denote the collection of all such path systems.

Definition 1. Given a path system \( \mathcal{P} \in \mathcal{P}_{G,N} \), a global packing of \((G, N, \mathcal{P})\) is a mapping \( \phi \) from \( \mathcal{P} \) to a set of \( k \) distinct labels, such that for any two dipaths \( P, \hat{P} \in \mathcal{P}, \phi(P) \neq \phi(\hat{P}) \) provided that \( P \) and \( \hat{P} \) have one or more than one arc in common. The global packing number of \((G, N, \mathcal{P})\), denoted by \( \Phi(G, N, \mathcal{P}) \), is the minimum integer \( k \) to guarantee the existence of a global packing. The global packing number of \((G, N)\), denoted by \( \Phi(G, N) \), is defined to be the smallest global packing number of \((G, N, \mathcal{P})\) among all path systems \( \mathcal{P} \in \mathcal{P}_{G,N} \); i.e.,

\[ \Phi(G, N) := \min_{\mathcal{P} \in \mathcal{P}_{G,N}} \Phi(G, N, \mathcal{P}). \]

A path system \( \mathcal{P} \) with \( \Phi(G, N, \mathcal{P}) = \Phi(G, N) \) is said to be ideal.

A path system is said to be symmetric if \( P_{(a, b)} \) and \( P_{(b, a)} \) contain the same links but opposite directions. A global packing is said to be symmetric if both \( P_{(a, b)} \) and \( P_{(b, a)} \) receive the same label. The symmetric global packing number over all symmetric path systems can be simply defined on the underlying simple graph \( G = (V, E) \), by considering both \( P_{(a, b)} \) and \( P_{(b, a)} \) as a non-directed path \( P_{(a, b)} \). Let \( \Phi^s(G, N) \) denote the symmetric global packing number of \((G, N)\). Since a symmetric global packing is also a global packing by viewing a link as two opposite arcs, we have

\[ \Phi(G, N) \leq \Phi^s(G, N). \] (1)

The global packing number and symmetric global packing number for a ring network have been solved in [7] and [8] respectively. The results show that the equality in (1) may not hold in some cases. In fact, for a ring of \( n \) nodes, \( R_n \), \( \Phi(R_n, V(R_n)) = n^2/8 \) and \( \Phi^s(R_n, V(R_n)) = 1 + n^2/8 \) when \( n \) is doubly even.
All terminologies and notations on graph theory used throughout this paper can be referred to the textbook written by D. B. West [19].

**B. Maximum link load and natural bounds**

Let \( G = (V, A) \) be a bidirected graph and \( N \) be a subset of \( V(G) \).

**Definition 2.** For \( P \in \mathfrak{P}_{G,N} \) define the link load of \( P \) by

\[
L(P) := \max_{a \in A(G)} \sum_{P \in \mathfrak{P}} 1_{P \cap a \neq \emptyset},
\]

where \( 1_s = 1 \) if \( s \) is true and \( 1_s = 0 \) otherwise. Then, the maximum link load (or, required capacity) of \( G \) with respect to \( N \) is defined by

\[
C(G, N) := \min_{P \in \mathfrak{P}_{G,N}} L(P).
\]

The maximum link load is a natural lower bound of global packing number, that is, for a graph \( G \) and any node set \( N \subset V(G) \) we have

\[
\Phi(G, N) \geq C(G, N). \tag{2}
\]

Let \( P \) be a path system of \( (G, N) \) and \( \phi \) be a global packing of \( (G, N, P) \). Then, for each label \( i \), the preimage of \( i \) under \( \phi \), \( \phi^{-1}(i) \), forms a set of mutually arc-disjoint dipaths. Another natural lower bound of \( \Phi(G, N) \) is given as follows.

**Proposition 3.** Let \( P \in \mathfrak{P}_{G,N} \) be a path system of \( (G, N) \). Then,

\[
\Phi(G, N, P) \geq \left\lfloor \frac{\sum_{P \in \mathfrak{P}} |A(P)|}{|A(G)|} \right\rfloor. \tag{3}
\]

**Proof.** Assume \( \Phi(G, N, P) = k \). Let \( \phi \) be a global packing of \( (G, N, P) \) with \( k \) labels, say \( 0, 1, \ldots, k-1 \). Since any set of mutually arc-disjoint dipaths consists of at most \( |A(G)| \) arcs in total, we have

\[
\sum_{P \in \mathfrak{P}} |A(P)| \leq |A(G)|, \tag{4}
\]

for \( i = 0, 1, \ldots, k-1 \). On the other hand, each dipath is assigned a unique label under \( \phi \), so it is clear that

\[
\sum_{P \in \mathfrak{P}} |A(P)| = \sum_{i=0}^{k-1} \left( \sum_{P \in \mathfrak{P}} |A(P)| \right). \tag{5}
\]

Combining (4) and (5) yields \( \sum_{P \in \mathfrak{P}} |A(P)| \leq k|A(G)|. \)

**C. Fat-tree networks**

A fat-tree topology is an efficient and economical architecture for data center networks. In an \( n \)-ary fat-tree there are \( n^2 \) 2n-port core switches and \( 2n \) pods, each pod contains two layers of \( n \) 2n-port switches: aggregation switches in the upper layer and edge switches in the lower layer. The core switches can be separated evenly into \( n \) groups. For \( 0 \leq i < n \), each switch of the \( i \)th core group has one port connected to the \( i \)th aggregation switch of each of \( 2n \) pods. That is, each aggregation switch is connected to \( n \) core switches. Each of the remaining \( n \) ports of aggregation switch is connected to the \( n \) edge switches in the same pod. Finally, each edge switch is directly connected to \( n \) hosts, which form a subnet.

Therefore, an \( n \)-ary fat-tree network can support up to \( 2n^3 \) hosts. There are three layers of arcs in a fat-tree. From top to bottom they are: between core and aggregation switches, between aggregation and edge switches, and between edge switches and hosts. We group them into three sets:

(i) \( C_1 \): The set of arcs between core and aggregation switches.

(ii) \( C_2 \): The set of arcs between aggregation and edge switches.

(iii) \( C_3 \): The set of arcs between edge switches and hosts.

Obviously, \( |C_1| = |C_2| = |C_3| = 4n^3 \) in an \( n \)-ary fat-tree. Fig. 1 illustrates the topology of 3-ary fat-tree. Note that in [20] an \( n \)-ary fat-tree refers to an \( n \)-port topology, instead of \( 2n \)-port in this paper.

Let \( T_n \) represent an \( n \)-ary fat-tree network so that the link between any two nodes represents a communication channel. There are four classes of nodes in \( T_n \): core-nodes for core switches, aggregation-nodes for aggregation switches, edge-nodes for edge switches, and host-nodes for hosts. We use \( c_{i,j} \) to denote the \( j \)th core-node in the \( i \)th core group, \( a_{i,j} \) to denote the \( j \)th aggregation-node in pod \( i \), \( e_{i,j} \) to denote the \( j \)th edge-node in pod \( i \), and \( h_{i,j} \) to denote the \( j \)th host-node in the subnet under the edge-node \( e_{i,j} \). Denoted by \( C_n, A_n, E_n \) and \( H_n \) the set of core-nodes, aggregation-nodes, edge-nodes and host-nodes in the fat-tree \( T_n \), respectively. Obviously, \( |C_n| = n^2 \), \( |A_n| = |E_n| = 2n^2 \) and \( |H_n| = 2n^3 \).

For a path system \( P \) of \( G \) with respect to \( N \) and a corresponding global packing \( \phi \), the packing set of arc \( a \in A(G) \), denoted by \( \phi_a \), is defined to be the set of labels it receives under \( \phi \). That is,

\[
\phi_a = \{ \phi(P) : P \cap a \neq \emptyset, P \in \mathfrak{P} \}.
\]

**Definition 4.** Let \( \phi \) be a global packing of a path system of \( T_n \). \( \phi \) is called well-balanced if for any two arcs \( a \) and \( b \) in the same group \( C_i, i = 1, 2, 3, \)

\[
\phi_a = \phi_b.
\]

**D. Latin squares**

For any positive integer \( n \), let \( Z_n := \{0, 1, 2, \ldots, n-1\} \) denote the ring of residues modulo \( n \). Let \( \mathfrak{S}_n \) be the set of permutations on \( Z_n \). We adopt the notation \( \pi = [\pi_0 \pi_1 \pi_2 \cdots \pi_{n-1}] \), where \( \pi_i = \pi(i) \) for \( i \in Z_n \), to denote a permutation \( \pi \in \mathfrak{S}_n \).

An \( n \) by \( n \) array with entries in \( Z_n \) is called a Latin square of order \( n \) if none of integers in \( Z_n \) occurs twice within any row or column. In other words, all rows and columns are permutations in \( \mathfrak{S}_n \). A Latin square of order \( n \), denoted by \( L = (l_{i,j}) \) for \( i = 0, 1, \ldots, n-1 \) and \( j = 0, 1, \ldots, n-1 \), is called unipotent (or fixed diagonal) if \( l_{i,j} = 0, \forall i \in Z_n \).

An \( n \) by \( n \) array defined on \( Z_n \) is a partial Latin square of order \( n \) if each integer in \( Z_n \) occurs at most once within any row or column. A partial Latin square \( L = (l_{i,j}) \) is called diagonal-free provided cell \( l_{i,j} \) is filled if and only if \( i \neq j \). We use diagonal-free square to denote a partial diagonal-free Latin square for short. It is worth mentioning that a diagonal-free square is different from a holey Latin square of type 1.
because it is always possible to extend a holey Latin square to a Latin square, but not for a diagonal-free square.

See Fig. 2 for examples of Latin square, unipotent Latin square and diagonal-free square of order 4.

![Diagram](image)

**Fig. 2.** (a) Latin square, (b) unipotent Latin square, and (c)-(d) diagonal-free squares of order 4.

Denote by \( \text{LS}(n) \) the set of all distinct Latin squares of order \( n \), \( \text{ULS}(n) \) the set of all distinct unipotent Latin squares of order \( n \), \( \text{DFS}(n) \) the set of all distinct diagonal-free squares of order \( n \), and by \( \text{ls}(n) \), \( \text{uls}(n) \), \( \text{dfs}(n) \) their respective cardinalities. Obviously, we can construct a diagonal-free square by deleting all diagonal entries of a unipotent Latin square. See Fig. 2(b) and Fig. 2(c) for instance. Therefore, \( \text{uls}(n) \leq \text{dfs}(n) \). Please refer to A002860 and A000479 in [21] for the values of \( \text{ls}(n) \) and \( \text{uls}(n) \), respectively. See [22] for more information on (unipotent) Latin squares.

Consider an \( n \)-ary fat-tree network \( T_n \). This paper investigates the global packing number of \( (T_n, N) \) for two cases: \( N = E_n \) and \( N = H_n \). The approach used here is to find lower bounds for the two cases by (2) and (3), and then to construct corresponding path systems by means of Latin squares, unipotent Latin squares and diagonal-free squares such that these lower bounds are achieved. It is worth mentioning that the use of such permutation structures on engineering problems is an old fashion and can be found in literatures, such as [23], [24] on recursive circulant networks (RCNs), [25] on reliable data broadcasting by independent spanning trees (ISTs), and [26] on cognitive radio networks (CR networks).

### III. Edge-to-Edge Traffic

Recall that \( E_n \) is the set of edge-nodes in \( T_n \). This section is devoted to the computation of global packing number of \( (T_n, E_n) \).

In \( E_n \) there are two classes of edge-node pairs: (i) both edge-nodes are in the same pod; and (ii) edge-nodes are in different pods. Let \( P_{(t,i),(t',i')} \) denote a dipath starting from edge-node \( e_{t,i} \) to \( e_{t',i'} \). Any path system of \( T_n \) with respect to \( E_n \) can be partitioned into two sets by

\[
\mathcal{E}_1 := \{ P_{(t,i),(t',i')} : t \in \mathbb{Z}_{2n}, i, i' \in \mathbb{Z}_n, i \neq i' \},
\]

\[
\mathcal{E}_2 := \{ P_{(t,i),(t',i')} : t, t' \in \mathbb{Z}_{2n}, i, i' \in \mathbb{Z}_n, t \neq t' \}.
\]

Note that \( |\mathcal{E}_1| = 2n^2(n-1) \) and \( |\mathcal{E}_2| = 2n^3(2n-1) \).

**Lemma 5.** For any integer \( n > 1 \), we have \( \Phi(T_n, E_n) \geq 2n \).

**Proof.** Since an edge-to-edge directed path does not contain any arc in \( \mathcal{E}_3 \), we have \( \Phi(T_n, E_n) = \Phi(T_n', E_n) \), where \( T_n' \) is the graph obtained from \( T_n \) by cutting off all host-nodes.

Let \( \mathcal{P} = \mathcal{E}_1 \cup \mathcal{E}_2 \) be an ideal path system of \( (T_n', E_n) \), and let \( \mathcal{P} = \mathcal{P}' \cup \mathcal{P}'' \) be an \( \mathcal{P}' \)-based canonical path system. Hence

\[
\sum_{P \in \mathcal{P}} |A(P)| \geq 2|\mathcal{E}_1| + 4|\mathcal{E}_2| = 4n^2(4n^2 - n - 1).
\]

By the fact that \( |A(T_n')| = 8n^3 \), we derive from Proposition 3 that

\[
\Phi(T_n', E_n) = \Phi(T_n', \mathcal{P}, E_n)
\]

\[
\geq \left\lceil \frac{\sum_{P \in \mathcal{P}} |A(P)|}{|A(T_n')|} \right\rceil = \left\lceil \frac{4n^2 - n - 1}{2n} \right\rceil = 2n,
\]

whenever \( n \geq 2 \). Hence \( \Phi(T_n, E_n) = \Phi(T_n', E_n) \geq 2n \).

**Construction 1 (First canonical path system).** Let \( D = (d_{i,j}) \) be a diagonal-free square of order \( n \), \( L = (l_{i,j}) \) be a Latin square of order \( n \), and \( \pi \) be a permutation on \( \mathbb{Z}_n \). The first \( (D, L, \pi) \)-based canonical path system \( \mathcal{P}^* = \mathcal{E}_1 \cup \mathcal{E}_2 \) of \( T_n \) with respect to \( E_n \), where \( \mathcal{E}_1, \mathcal{E}_2 \) are as defined in (6)-(7), is defined as

(i) for \( P_{(t,i),(t',i')} \in \mathcal{E}_1 \) let

\[
P_{(t,i),(t',i')} = e_{t,i} \rightarrow a_{t,d_{i,i'}} \rightarrow e_{t',i'};
\]

(ii) for \( P_{(t,i),(t',i')} \in \mathcal{E}_2 \) let

\[
P_{(t,i),(t',i')} := e_{t,i} \rightarrow a_{t,l_{i,i'}} \rightarrow e_{t',i'} \rightarrow a_{t',l_{i,i'}} \rightarrow e_{t',i'}.
\]

See Fig. 3–4 for an illustration.
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Example 1. Consider a 3-ary fat-tree. Let $D$ and $L$ be the diagonal-free square and Latin square shown in Fig. 5, and let $\pi = [1\ 2\ 0]$ be a permutation in $S_3$.

Following Construction 1, in Pod 1 the edge-node $e_{1,1}$ connects to $e_{1,2}$ via $a_{1,1}$, while $e_{1,2}$ connects to $e_{1,1}$ via $a_{1,0}$ in reverse. And, the dipaths connecting $e_{0,1}$ and $e_{2,2}$ are set to be

$$e_{0,1} \rightarrow a_{0,1} \rightarrow e_{1,2} \rightarrow a_{2,1} \rightarrow e_{2,2}$$

and

$$e_{2,2} \rightarrow a_{2,2} \rightarrow e_{2,0} \rightarrow a_{0,2} \rightarrow e_{0,1},$$

since $\pi_1 = 2$ and $\pi_2 = 0$.

Lemma 6. For any integer $n > 1$, we have $\Phi(T_n, E_n) \leq 2n$.

Proof. Let $P^*$ be a $(D, L, \pi)$-based canonical path system of $T_n$ with respect to $E_n$ for some $D \in \text{DFS}(n), L \in \text{LS}(n)$ and $\pi \in S_n$. It suffices to find a global packing from $P^*$ to a set of $2n$ distinct labels, say $\{0, 1, \ldots, 2n - 1\}$.

Recall that $P^* = E_1 \sqcup E_2$, where $E_1$ is the set of dipaths whose endpoints are in the same pod and $E_2$ otherwise. Let $F = (f_{i,j})$ be a unipotent Latin square of order $2n$. The mapping $\phi^*$ is defined by

$$\phi^*(P) = \begin{cases} 0, & \text{if } P \in E_1; \\ f_{i,j}, & \text{if } P = P_{(t,i),(t',i')} \in E_2. \end{cases}$$

(11a)

(11b)

Now, we claim $\phi^*$ is a global packing of $(T_n, E_n)$. Suppose $P \neq \bar{P}$ have one common arc, $\kappa$, and $\phi^*(P) = \phi^*(\bar{P}) = \lambda$.

Case 1. $\lambda = 0$. In this case, $\kappa$ is an arc between edge- and aggregation-nodes in some pod $t$. By symmetry, let $\kappa = (e_{t,i}, a_{t,k})$ for some $i$ and $k$. Assume $e_{t,j}$ and $e_{t,j'}$ are the ending nodes of $P$ and $\bar{P}$, respectively, where $i, j, j'$ are all distinct. By (9), $d_{i,j} = k = d_{i',j'}$, which is a contradiction since $D$ is a diagonal-free square.

Case 2. $\lambda > 0$. In this case, $\kappa$ may be an arc in $C_1$ or $C_2$. First, consider $\kappa \in C_2$. Assume by symmetry the case that $\kappa = (e_{t,i}, a_{t,k})$ for some $t, i$ and $k$; that is, $P$ and $\bar{P}$ have common starting node. Since $F$ is a unipotent Latin square, there exists a unique column index, say $t'$, such that $f_{t,i} = \lambda$. Therefore, the ending nodes of $P$ and $\bar{P}$ are both in pod $t'$. Assume $e_{t',j} \neq e_{t',j'}$ and let $P^*$ be these two ending nodes. We now consider two subcases: $j = j'$ and $j \neq j'$. If $j = j'$, we have $P = \bar{P}$ because dipaths are uniquely determined by their starting and ending nodes by (10). If $j \neq j'$, we have $\ell_{i,j} = k = \ell_{i',j'}$, a contradiction to that $L$ is assumed to be a Latin square. It remains to consider that $\kappa \in C_1$. Again, consider by symmetry the case that $\kappa = (a_{t,k}, c_{t,x})$ for some $t, k$ and $x$. Notice that $P$ and $\bar{P}$ have the same aggregation-node $a_{t,k}$, so they are from the same pod $t$. Assume the two starting nodes are $e_{t,i}$ and $e_{t,i'}$ for some $i$ and $i'$. By (10), we have $x = x = \pi_1$, and thus $i = \hat{i}$. Therefore, $P$ and $\bar{P}$ have a common arc in $C_2$, which yields a contradiction by the argument above. Hence we complete the proof. □

Theorem 7. For any integer $n > 1$, we have $\Phi(T_n, E_n) = 2n$.

Moreover, the packing sets of all arcs in $T_n$ under the global packing $\phi^*$ defined in (11a), (11b) are of minimal size, and

(i) for any arc $a \in C_1$, $|\phi^*_a| = 2n - 1$;

(ii) for any arc $a \in C_2$, $2n - 1 \leq |\phi^*_a| < 2n$.

Proof. The conclusion that $\Phi(T_n, E_n) = 2n$ directly follows from Lemma 5 and Lemma 6. Given a global packing $\phi$ of a path system $P$, one can study the load-balancing performance of it by counting the cardinality of the set

$$\Pi_{\phi, P} := \{(a, \ell) : a \in A(T_n), \text{ arc } a \text{ receives label } \ell \text{ under } \phi\}.$$

For a fixed dipath $P \in P$, every arc receives a unique label, so $P$ can support a number of $|A(P)|$ ordered pairs to the set $\Pi_{\phi, P}$. By going through all dipaths, by (8), we have

$$|\Pi_{\phi, P}| = \sum_{P \in P} |A(P)| \geq 4n^2 (4n^2 - n - 1).$$

(12)

Now, consider the structure of the first canonical path system, $P^*$, and the global packing, $\phi^*$, defined in Lemma 6. By (10) and (11b), every arc in $C_1 \cup C_2$ receives a label from $\{1, 2, \ldots, 2n - 1\}$. Since label 0 is assigned to dipaths in $E_1$, where all arcs are from $C_2$, we have

$$\phi^*_a = \{1, 2, \ldots, 2n - 1\}, \text{ for } a \in C_1.$$

It suffices to consider the arcs in $C_1$ which do not receive label 0, in other words, do not appear in $E_1$. By (6) and (9), they are those of the form $(e_{t,i}, a_{t,d_{t,i}})$ or $(a_{t,d_{t,i}}, e_{t,i})$, where $D =$
(d_{i,j}) is the given diagonal-free square of order n. Therefore, for any arc a ∈ C_2,

\[ \phi^* \cap \{1, 2, \ldots, 2n - 1\} = \begin{cases} \{e_{t,i}, a_{t,d_{i,j}}\} & \text{if } a = (e_{t,i}, a_{t,d_{i,j}}) \text{ or } (a_{t,d_{i,j}}, e_{t,i}) \text{ for some } t, i, j, \vspace*{1pt} \\
\emptyset & \text{otherwise.} \end{cases} \]

Note that there are exactly 4n^2 arcs in C_2 having packing set \{1, 2, \ldots, 2n - 1\}. It is also clear that for a ∈ C_3, \phi^* = \emptyset. By \( |C_1| = |C_2| = 4n^2 \), we have

\[ \|I_{\phi^*, \pi^*}\| = 4n^3(2n - 1) + 4n^2(2n - 1) + (4n^3 - 4n^2)2n = 4n^2(4n^2 - 1), \]

which attains the minimum value in (12).

**Corollary 8.** For any integer \( n > 1 \), there exist at least

\[ (dfs(n))^2 \cdot (1s(n) \cdot n!^2)^2 \]

ideal path systems of \( T_n \) with respect to \( E_n \).

**Proof.** Lemma 6 proved that any \((D, \mathcal{L}, \pi)\)-based canonical path system \( \mathcal{P}^* \) is \( E_1 \cup E_2 \) is ideal. In fact, we essentially have proved (in Case 1 of Lemma 6) that all dipaths in the same pod are mutually arc-disjoint if the connection is based on a diagonal-free square. Then, we can use a different diagonal-free square for each pod. Similarly, we have proved (in Case 2 of Lemma 6) that all dipaths between any two pods are mutually arc-disjoint if the connection is based on a Latin square and a permutation. So, we can use a different Latin square and a different permutation for each pair of distinct pods \( t, t' \), respectively. Hence the result follows from the fact that there are 2n pods in \( T_n \).

\[ \Box \]

**IV. HOST-TO-HOST TRAFFIC**

Recall that \( H_n \) is the set of host-nodes in \( T_n \). This section is devoted to the computation of global packing number of \((T_n, H_n)\). We first derive its lower bound.

**Lemma 9.** For any positive integer \( n \), we have \( \Phi(T_n, H_n) \geq 2n^3 - 1 \).

**Proof.** Any host-to-host dipath with a given starting node, say \( h_{t,i,j} \), must pass through the edge-node \( e_{t,i} \) and the arc \((h_{t,i,j}, e_{t,i})\). So, the maximum link load of \( T_n \) with respect to \( H_n \) is at least \( 2n^3 - 1 \), which implies that \( \Phi(T_n, H_n) \geq C(T_n, H_n) \geq 2n^3 - 1 \) by (2).

In \( H_k \) there are three classes of host-node pairs: (i) both host-nodes are in the same subnet; (ii) both host-nodes are in the same pod but not in the same subnet; and (iii) host-nodes are in different pods. Let \( P(t,i,j), (t',i',j') \) denote a dipath from host-node \( h_{t,i,j} \) to \( h_{t',i',j'} \). Any path system of \( T_n \) with respect to \( H_k \) can be partitioned into three sets by

\[ H_1 := \{ P(t,i,j), (t',i',j') : t \in \mathbb{Z}_{2n}, i, j, j' \in \mathbb{Z}_n, j \neq j' \}, \]

\[ H_2 := \{ P(t,i,j), (t',i',j') : t \in \mathbb{Z}_{2n}, i, i', j, j' \in \mathbb{Z}_n, i \neq i' \}, \]

and

\[ H_3 := \{ P(t,i,j), (t',i',j') : t, t' \in \mathbb{Z}_{2n}, i, i', j, j' \in \mathbb{Z}_n, t \neq t' \}. \]

Note that \( |H_1| = 2n^3(n - 1) \), \( |H_2| = 2n^4(n - 1) \), and \( |H_3| = 2n^5(2n - 1) \).

Now, we define the host-to-host version of canonical path system of \( T_n \) with respect to \( H_n \).

**Construction 2** (Second canonical path system). Let \( L = (t_{i,j}) \) be a Latin square of order \( n \) and \( \pi \in \mathcal{S}_n \). The second \((L, \pi)\)-based canonical path system \( \mathcal{P}^2 = H_1 \cup H_2 \cup H_3 \) of \( T_n \) with respect to \( H_n \), where \( H_1, H_2, H_3 \) are defined in (13) – (15), is defined as

(i) for \( P(t,i,j), (t',i',j') \in H_1 \) let

\[ P_{t,i,j}, (t',i',j') := h_{t,i,j} \rightarrow e_{t,i} \rightarrow h_{t',i',j'}; \]

(ii) for \( P(t,i,j), (t',i',j') \in H_2 \) let

\[ P_{t,i,j}, (t',i',j') := h_{t,i,j} \rightarrow e_{t,i} \rightarrow a_{t,i,j} \rightarrow e_{t',i'} \rightarrow h_{t',i',j'}; \]

(iii) for \( P(t,i,j), (t',i',j') \in H_3 \) let

\[ P_{t,i,j}, (t',i',j') := h_{t,i,j} \rightarrow e_{t,i} \rightarrow a_{t,i,j} \rightarrow c_{t,i',j'} \rightarrow a_{t',i',j'} \rightarrow e_{t',i'} \rightarrow h_{t',i',j'} \].

See Fig. 6 for an illustration of (17).
\[ M^{(3)} = (m_{i,j}^{(3)}) \] be three Latin squares (may be the same) of order \( n \). The mapping \( \phi^P \) is defined as

(i) for \( P = P(t,i,j),(t,i,j') \in \mathcal{H}_1 \) let

\[ \phi^P = f_{j,j'}; \tag{20} \]

(ii) for \( P = P(t,i,j),(t',i',j') \in \mathcal{H}_2 \) let

\[ \phi^P = n \cdot f_{i,i'} + m_{j,j'}^{(1)}; \tag{21} \]

(iii) for \( P = P(t,i,j),(t',i',j') \in \mathcal{H}_3 \) let

\[ \phi^P = n^2 \cdot g_{i,i'} + n \cdot m_{i,j}^{(1)} + m_{j,j'}^{(1)}; \tag{22} \]

Now, we claim that \( \phi^P \) is a global packing of \( (T_n, H_n) \). Suppose \( P \neq \hat{P} \) have one common arc, \( \kappa \), and \( \phi^P(P) = \phi^P(\hat{P}) = \lambda \). Since, by (20)–(22), the images of \( \mathcal{H}_1 \), \( \mathcal{H}_2 \) and \( \mathcal{H}_3 \) under \( \phi^P \) are \( \phi^P(\mathcal{H}_1) = \{1,2,\ldots,n-1\} \), \( \phi^P(\mathcal{H}_2) = \{n,n+1,\ldots,n^2-1\} \) and \( \phi^P(\mathcal{H}_3) = \{n^2,n^2+1,\ldots,2n^3-1\} \), we have the following three cases according to the value of \( \lambda \).

**Case 1.** \( 1 \leq \lambda \leq n-1 \). In this case, we have \( P, \hat{P} \in \mathcal{H}_1 \), and thus \( \kappa \) is a host-to-edge arc in \( C_3 \). By symmetry, assume \( h_{i,i,j'} \) is the common starting node of \( P \) and \( \hat{P} \), whose ending nodes are \( h_{i,j}^{t,i,j'} \) and \( \hat{h}_{i,j}^{t,i,j'} \), respectively, for some \( i, j, j' \) and \( \hat{j} \). Note that \( j \neq j' \neq \hat{j} \). By (20), \( f_{j,j'} = \kappa = f_{j,j'} \), which is a contradiction to that \( \mathcal{F} \) is a unipotent Latin square.

**Case 2.** \( n \leq \lambda \leq n^2-1 \). In this case, we have \( P, \hat{P} \in \mathcal{H}_2 \), and thus \( \kappa \) may be an arc in \( C_2 \) or \( C_3 \). Assume \( P = P(t,i,j),(i,j') \) and \( \hat{P} = P(t,i,j),(i,j') \) for some \( i, j, i', j' \) and \( i, j, i', j' \). Since \( \phi^P(P) = \phi^P(\hat{P}) \), by (21) we have

\[ \begin{align*}
&f_{i,i'} = f_{i,i'}, \tag{23a} \\
&m_{j,j'}^{(1)} = m_{j,j'}^{(1)}. \tag{23b}
\end{align*} \]

If \( \kappa \in C_3 \), then either \( i = i', j = j' \) or \( \hat{i} = \hat{i}', \hat{j} = \hat{j}' \). Either case will imply \( (i,i',j,j') = (\hat{i},\hat{i}',\hat{j},\hat{j}') \) by (23a)–(23b), namely, \( P = \hat{P} \), a contradiction. The subcase that \( \kappa \in C_2 \) can be dealt with in the same way, and the proof is omitted.

**Case 3.** \( n^2 \leq \lambda \leq 2n^3-1 \). In this case, we have \( P, \hat{P} \in \mathcal{H}_3 \). Assume \( P = P(t,i,j),(i,j') \) and \( \hat{P} = P(t,i,j),(i,j') \) for some \( i, j, i', j' \) and \( i, j, i', j' \). Since \( \phi^P(P) = \phi^P(\hat{P}) \), by (22) we have

\[ \begin{align*}
&g_{i,j'} = g_{i,j'}, \tag{24a} \\
&m_{i,j'}^{(2)} = m_{i,j'}^{(2)}, \tag{24b} \\
&m_{j,j'}^{(3)} = m_{j,j'}^{(3)}. \tag{24c}
\end{align*} \]

Since \( P \) and \( \hat{P} \) are assumed to have common arcs, we only need to consider the case that \( \{i,i'\} = \{i,i'\} \) by (24a). Without loss of generality, suppose \( t = \hat{t} \) and \( t' = \hat{t}' \). Notice that \( P \) and \( \hat{P} \) are obtained by appending host-nodes to associated edge-to-edge dipaths which are defined in (10).

Since we have proved in the case 2 of the proof of Lemma 6 that, all edge-to-edge dipaths between any two fixed nodes are mutually arc-disjoint, it remains to consider that \( \kappa \) is a host-edge arc. Then, we have either \( i = i', j = j' \) or \( \hat{i} = \hat{i}', \hat{j} = \hat{j}' \).

By (24b)–(24c), we have \((i,i',j,j') = (\hat{i},\hat{i}',\hat{j},\hat{j}')\), namely, \( P = \hat{P} \), a contradiction. Hence we complete the proof. \( \square \)

Lemma 10 not only shows that each \((L,\pi)\)-based canonical path system defined in Construction 2 is ideal, it also highlights that the path system is load-balancing-wise “optimal” as being made precise in the following Theorem:

**Theorem 11.** For any positive integer \( n \), we have \( \Phi(T_n,H_n) = 2n^3-1 \). Moreover, the global packing defined in (20)–(22) is well-balanced and the packing sets of all arcs in \( T_n \) are of minimal size with

(i) for any arc \( a \in C_1 \), \( |\phi_a^P| = n(2n^2-1) \);

(ii) for any arc \( a \in C_2 \), \( |\phi_a^P| = n^2(2n-1) \); and

(iii) for any arc \( a \in C_3 \), \( |\phi_a^P| = \Phi(T_n,H_n) = 2n^3-1 \).

**Proof.** \( \Phi(T_n,H_n) = 2n^3-1 \) is directed from Lemma 9 and Lemma 10. From the definition of \( \phi^P \) and the structure of the second canonical path system \( P^* \), one has

\[ |\phi_a^P| = \begin{cases} 
\{n^2,n^2+1,\ldots,2n^3-1\}, & \text{if } a \in C_1, \\
\{n,n+1,\ldots,2n^3-1\}, & \text{if } a \in C_2, \\
\{1,2,\ldots,2n^3-1\}, & \text{if } a \in C_3.
\end{cases} \]

Then, the result follows by using the same counting method as in the proof of Theorem 7. \( \square \)

**Corollary 12.** For any positive integer \( n \), there exist at least \( (\text{Is}(n) \cdot n!)^{2(n-1)} \) ideal path systems of \( T_n \) with respect to \( H_n \).

**Proof.** The proof is similar to that of Corollary 8, and is omitted. \( \square \)

V. GLOBAL PACKING NUMBERS V.S. MAXIMUM LINK LOAD

Our results in previous sections reveal that the both sides of equation (2) are identical for the case that \( G = T_n \) and \( N = E_n \) or \( H_n \). That is,

\[ \Phi(T_n,E_n) = C(T_n,E_n) \]

and

\[ \Phi(T_n,H_n) = C(T_n,H_n). \]

In fact, it has been conjectured in [10] that \( \Phi(G,V(G)) = C(G,V(G)) \) for any bidirected graph \( G \), and the conjecture is proved to be true for some particular graphs: cycles [7], trees [11], and some Cartesian product graphs [9, [12]. Based on our results, it would be interesting to consider if \( \Phi(G,N) = C(G,N) \) for any bidirected graph \( G \) and for any set of nodes \( N \subseteq V(G) \).

When it comes to symmetric global packing numbers, following (1) and (2) we also have

\[ \Phi^s(G,N) \geq C(G,N). \tag{25} \]

It would be more difficult to make the equality hold in (25) than in (2), since there may be a gap between symmetric global packing number and global packing number. Here, we take the complete binary tree as an example.
Let $T$ be a complete binary tree of height 3, see Fig. 7. It is not hard to see that $C(T, V(T)) = 56$ and $(r, x)$ or $(x, r)$ is the arc where the maximum link load occurs. There is a unique path system of $(T, V(T))$, since there is only one path between any two nodes. Let us consider $\Phi(T, V(T))$. Suppose $\Phi(T, V(T)) = 56$ and $\phi$ is an ideal symmetric global packing. Let $P_1$ be the set of these 56 paths that contain the common link $\{x, r\}$. Let $P_2$ is the collection of paths connecting the nodes of the subtree under $y$ and that under $z$. Obvious, $|P_2| = 9$. Since any path in $P_2$ contains links $\{x, y\}$ and $\{x, z\}$, the nine paths must receive distinct symbols under $\phi$, i.e., $|\phi(P_2)| = 9$. On the other hand, among $P_1$ there are exactly 8 paths that do not contain links $\{x, y\}$ and $\{x, z\}$, namely, those paths connecting $x$ with $r$ and the nodes on the right subtree under $r$. So the remaining paths in $P_1$ must receive different labels from $\phi(P_2)$. Therefore, we have $|\phi(P_1 \cup P_2)| \geq 56 - 8 + 9 = 57$, a contradiction to $\Phi(T, V(T)) = 56$. Note that in [11] it has been shown that $\Phi(T, V(T)) = C(T, V(T))$. Hence in this example we conclude that $\Phi^*(T, V(T)) > \Phi(T, V(T)) = C(T, V(T))$.

VI. CONCLUDING REMARKS

The global packing number is a newly defined index that characterizes the capacity required to support a uniformly loaded communication network. This paper focuses on the fat-tree networks, which are popular choices for data center communication architecture. Based on the notion of Latin square, we compute the global packing number for a fat-tree network with respect to the set of all edge switches or hosts, and provide a construction algorithm for ideal path systems. Our result also shows that the global packing number for a fat-tree network is identical to the maximum link load.

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REFERENCES


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